



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hiroyuki Enomoto, et al.  
Serial No.: 09/019,087  
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For: Manufacturing Method of  
Semiconductor IC Device

Docket No.: TIJ-24816  
Art Unit: 1765  
Examiner: Perez Ramos, V  
Confirm. No.: 1640

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SUPPLEMENTAL RESPONSE UNDER 35 U.S.C. § 1.116

Assistant Commissioner For Patents  
Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that the above correspondence is being deposited with the US Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231.

William B. Kempler

Date

1/23/03

Sir:

In the Claims:

Please cancel Claim 11 without prejudice.

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TC 1700

REMARKS

Claim 11 has been cancelled without prejudice as superfluous in order to place the application in better condition for appeal.

Respectfully submitted,

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